FIG. 1

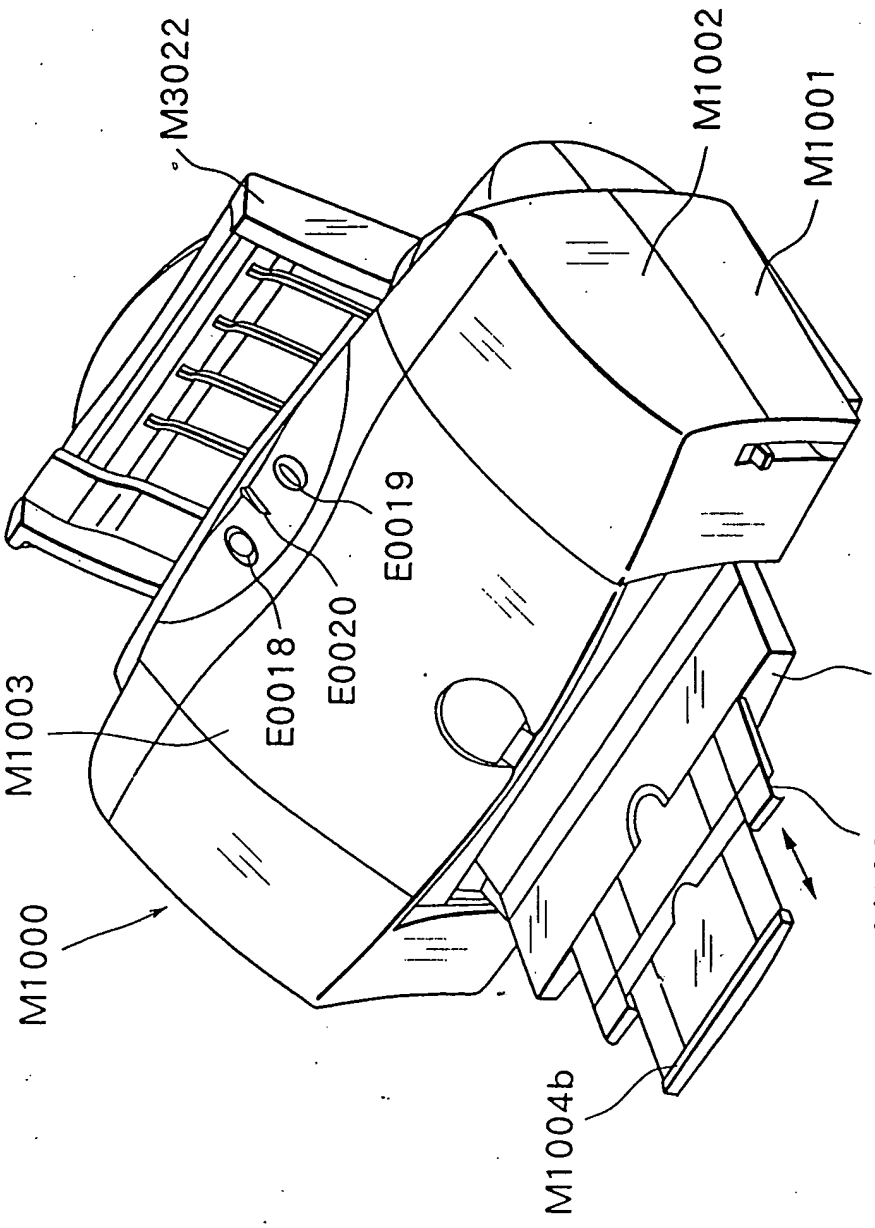


FIG. 2

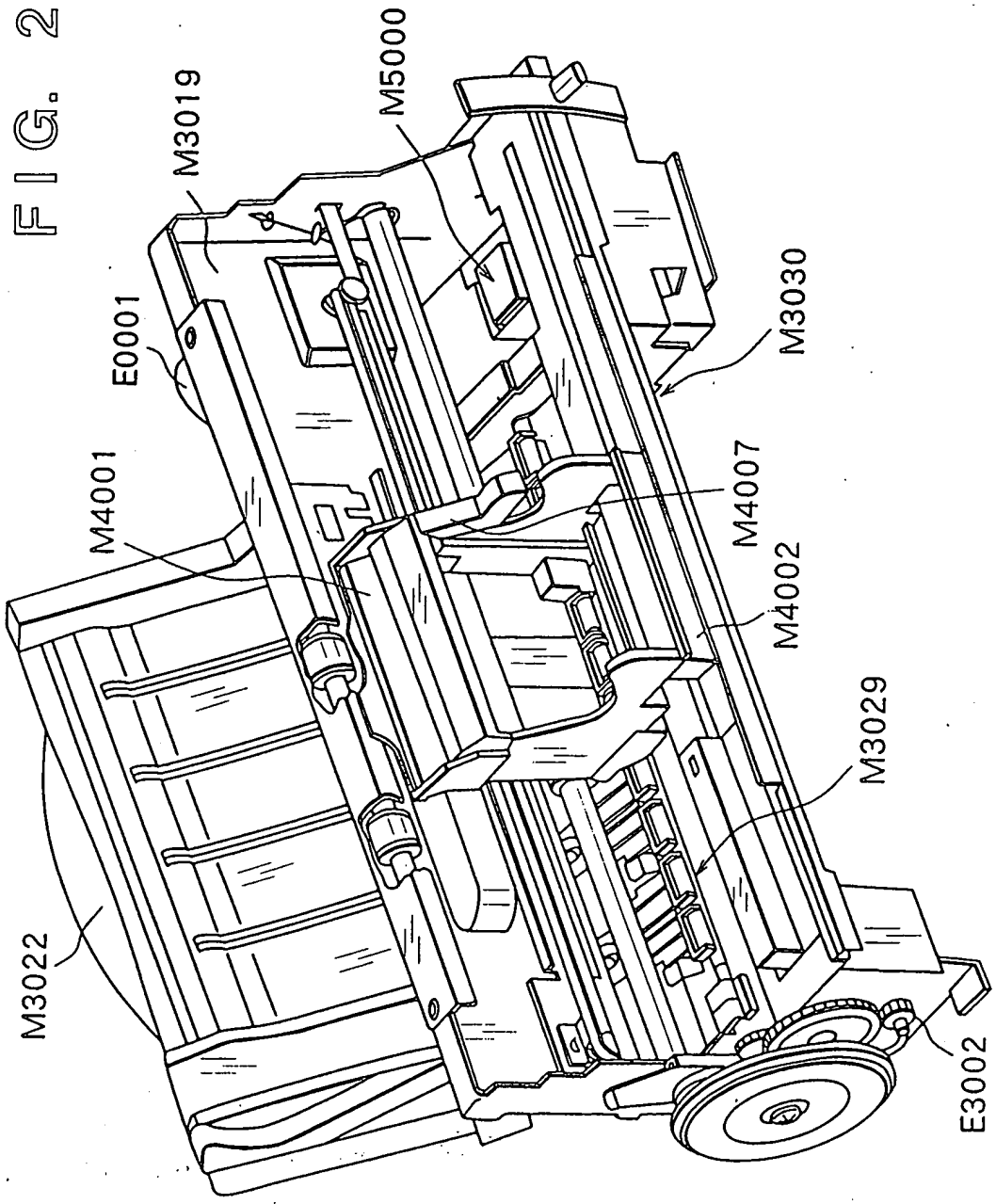


FIG. 3

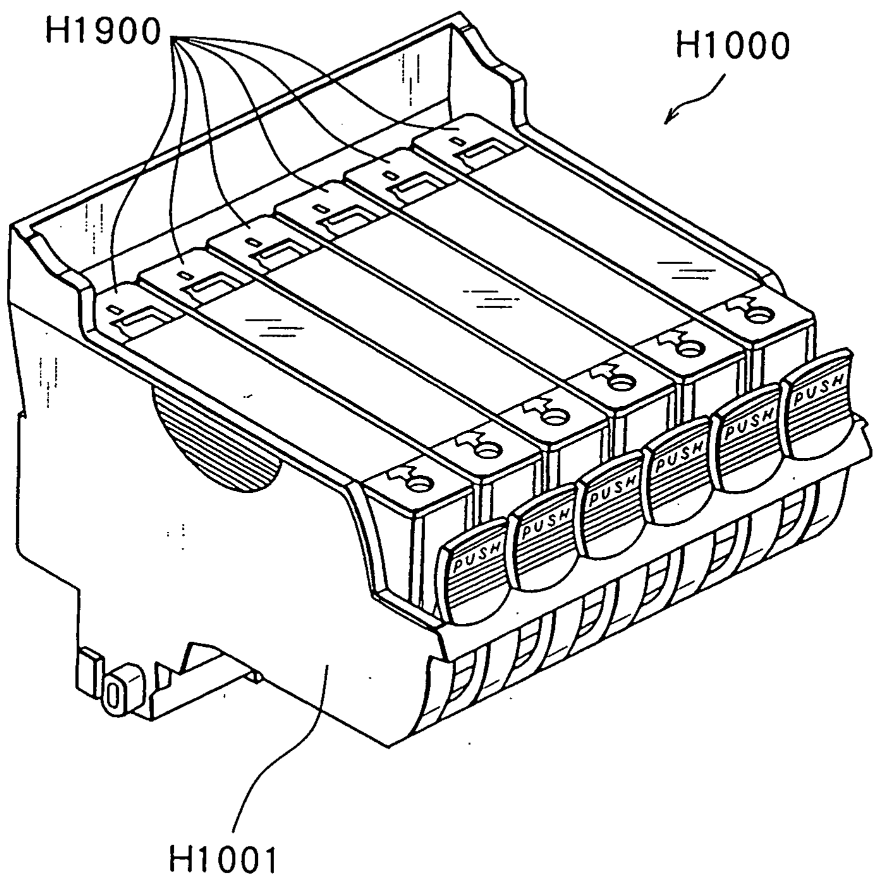


FIG. 4

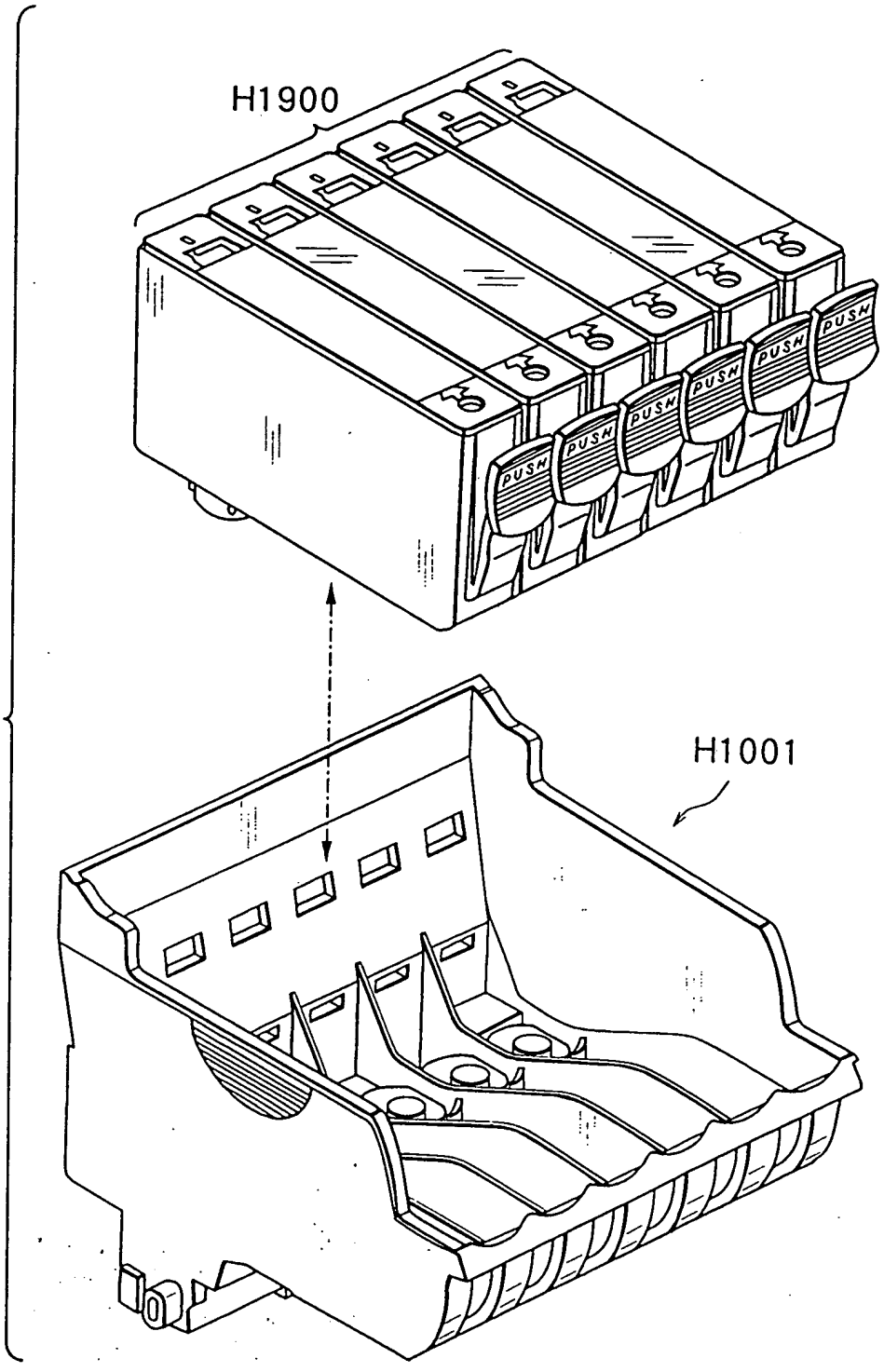


FIG. 5

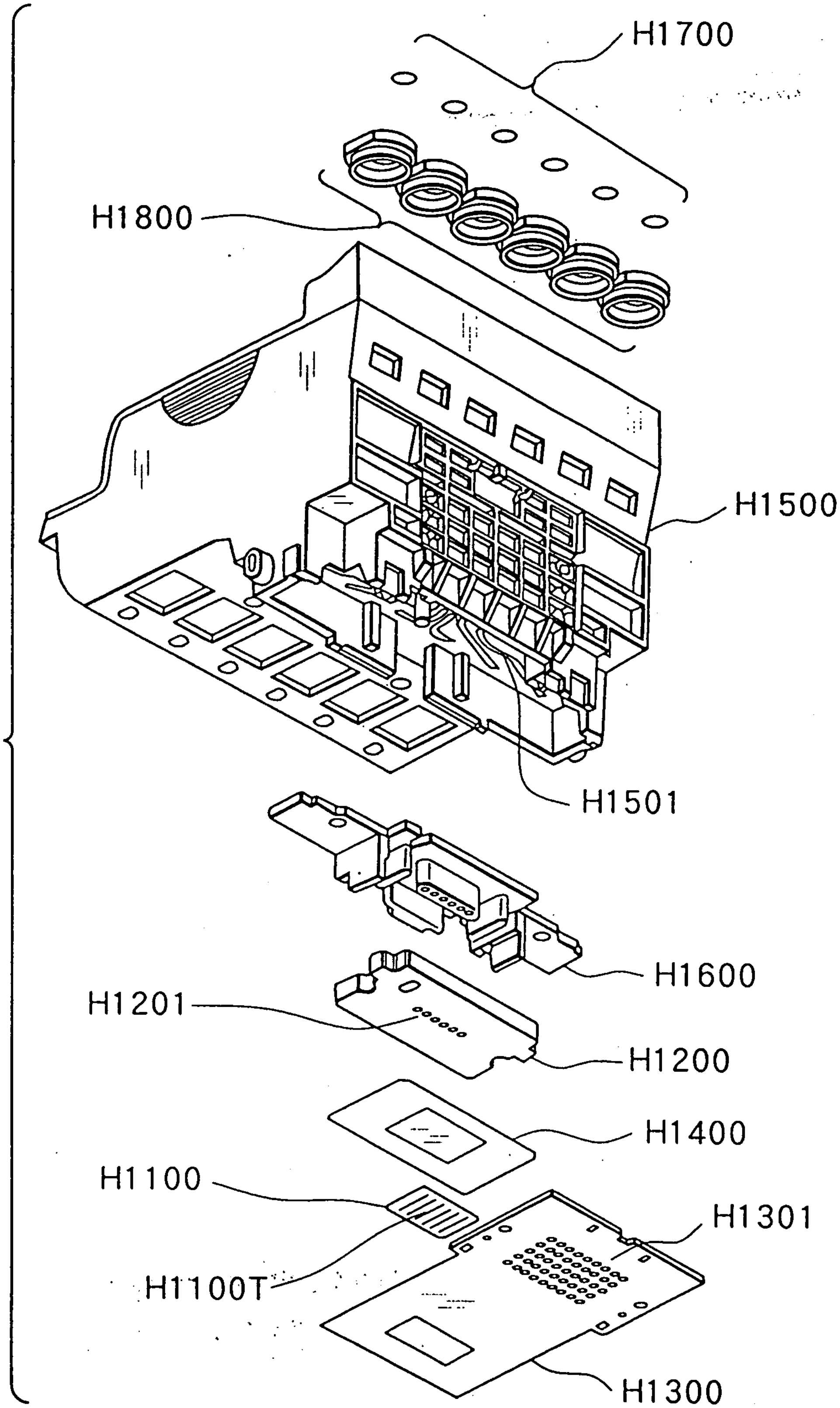

H1700
H1800
H1500
H1501
H1600
H1201
H1200
H1400
H1100
H1301
H1100T
H1300

FIG. 6A

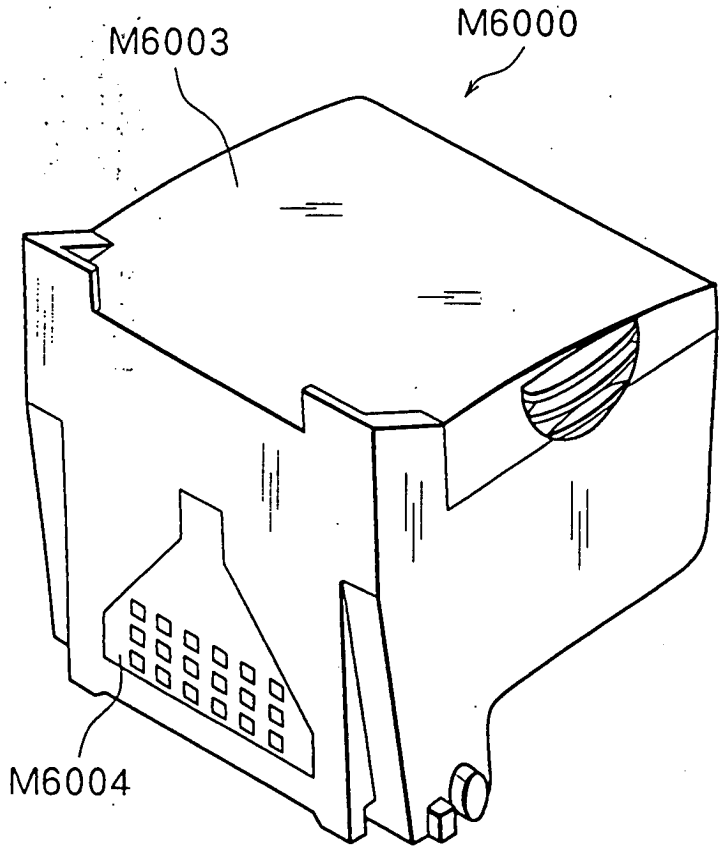


FIG. 6B

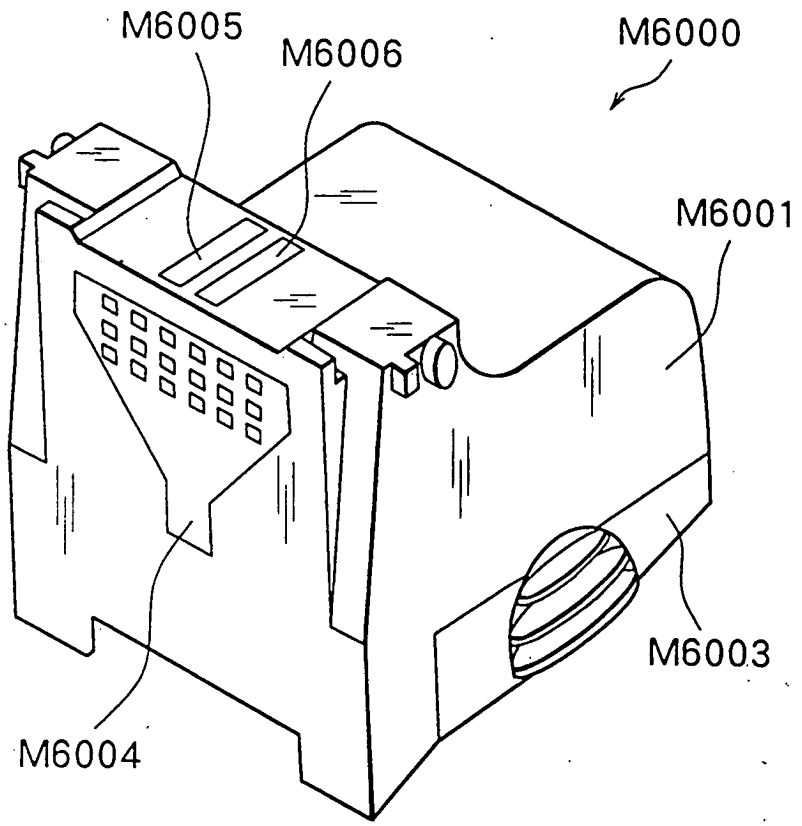


FIG. 7

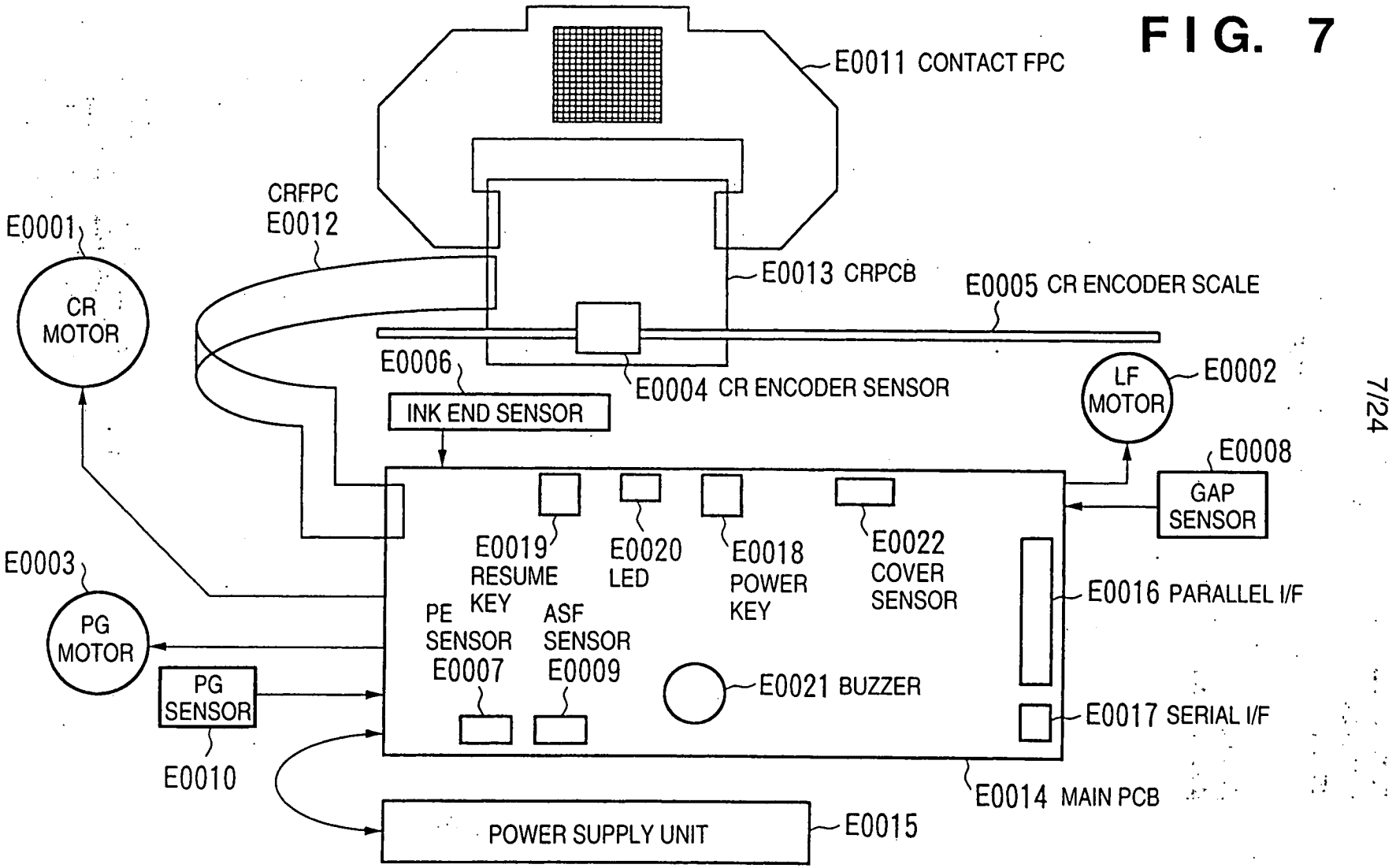


FIG. 8

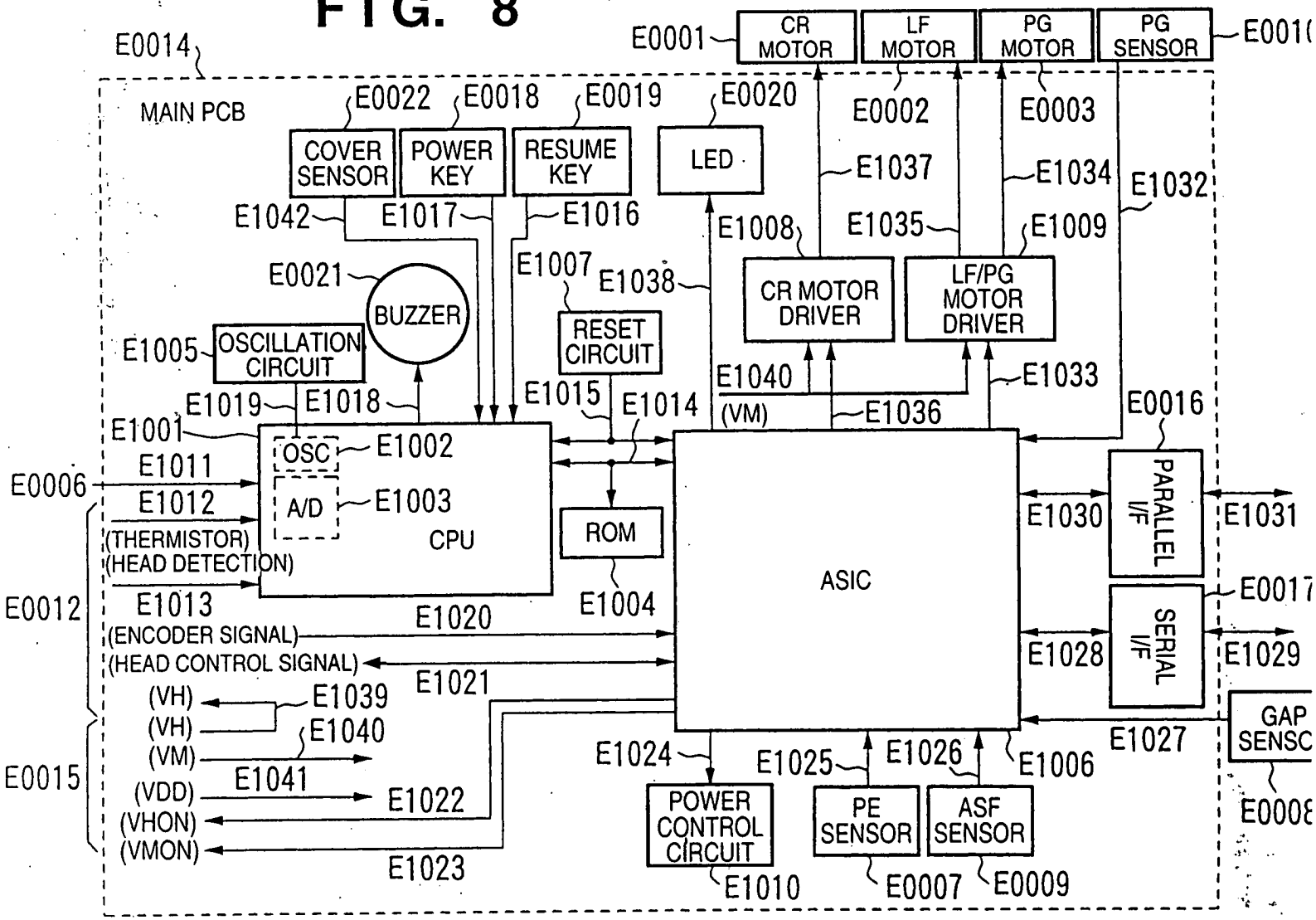


FIG. 9

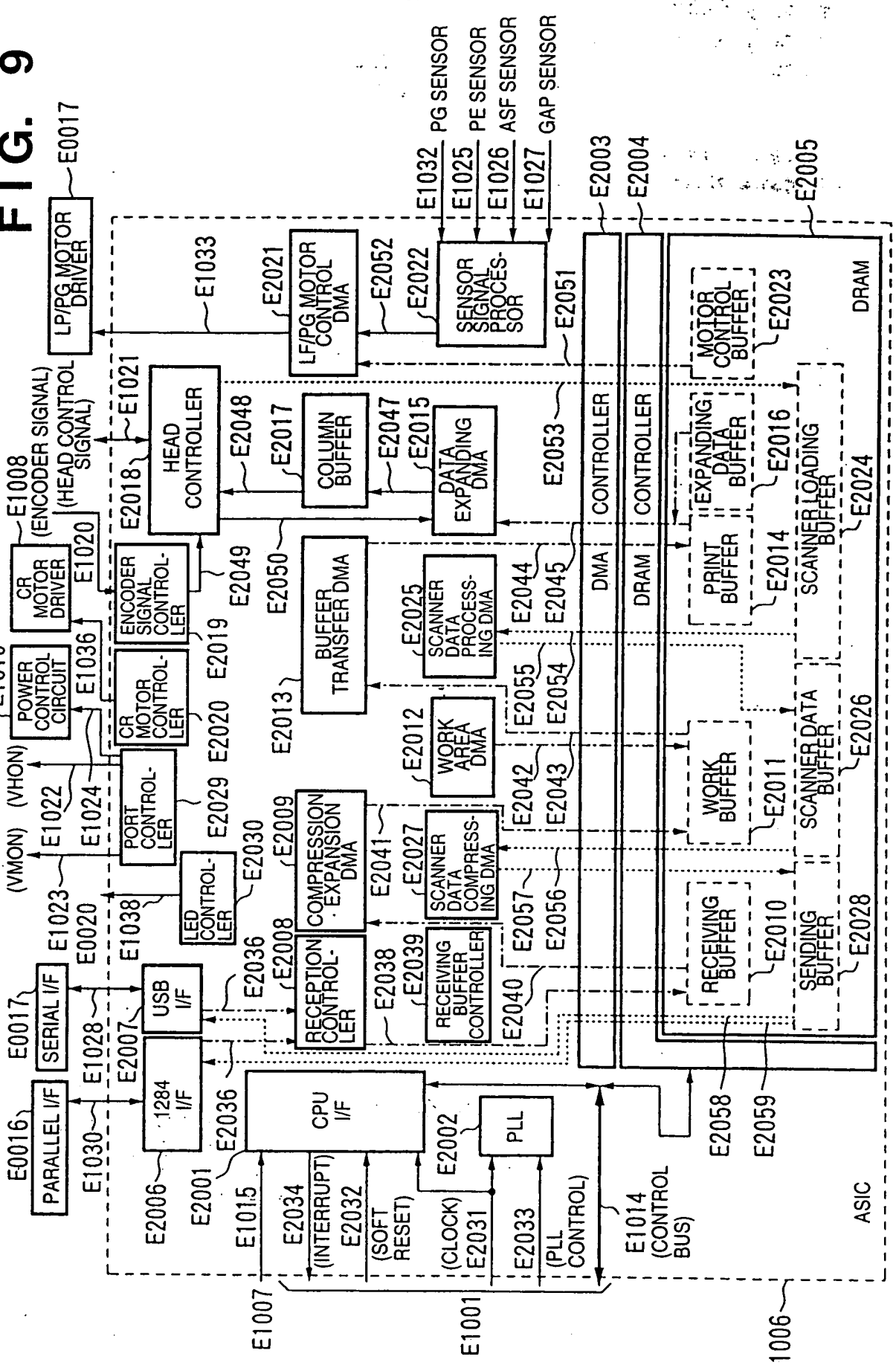

1006
ASIC
E1001
E1007
E1014 (CONTROL BUS)
E1015
E2001
E2002
E2003
E2004
E2005
E2006
E2007
E2008
E2009
E2010
E2011
E2012
E2013
E2014
E2015
E2016
E2017
E2018
E2019
E2020
E2021
E2022
E2023
E2024
E2025
E2026
E2027
E2028
E2029
E2030
E2031
E2032
E2033
E2034
E2036
E2038
E2039
E2040
E2041
E2042
E2043
E2044
E2045
E2046
E2047
E2048
E2049
E2050
E2051
E2052
E2053
E2054
E2055
E2056
E2057
E2058
E2059
E0016
E0017
E1008
E1010
E1020
E1021
E1022
E1023
E1024
E1025
E1026
E1027
E1028
E1030
E1032
E1033
E1036
E1038
CPU I/F
1284 I/F
PARALLEL I/F
SERIAL I/F
USB I/F
PLL
(CLOCK)
(SOFT RESET)
(INTERRUPT)
(PLL CONTROL)
PORT CONTROLLER
LED CONTROLLER
RECEPTION CONTROLLER
RECEIVING BUFFER CONTROLLER
COMPRESSION EXPANSION DMA
SCANNER DATA COMPRESSING DMA
WORK AREA DMA
BUFFER TRANSFER DMA
SCANNER DATA PROCESSING DMA
DATA EXPANDING DMA
COLUMN BUFFER
HEAD CONTROLLER
ENCODER SIGNAL CONTROLLER
CR MOTOR CONTROLLER
POWER CONTROL CIRCUIT
CR MOTOR DRIVER
LP/PG MOTOR DRIVER
LF/PG MOTOR CONTROL DMA
SENSOR SIGNAL PROCESSOR
(VMON)
(VHON)
(ENCODER SIGNAL)
(HEAD CONTROL SIGNAL)
PG SENSOR
PE SENSOR
ASF SENSOR
GAP SENSOR
DMA CONTROLLER
DRAM CONTROLLER
DRAM
RECEIVING BUFFER
SENDING BUFFER
WORK BUFFER
PRINT BUFFER
SCANNER DATA BUFFER
SCANNER LOADING BUFFER
EXPANDING DATA BUFFER
MOTOR CONTROL BUFFER

FIG. 10

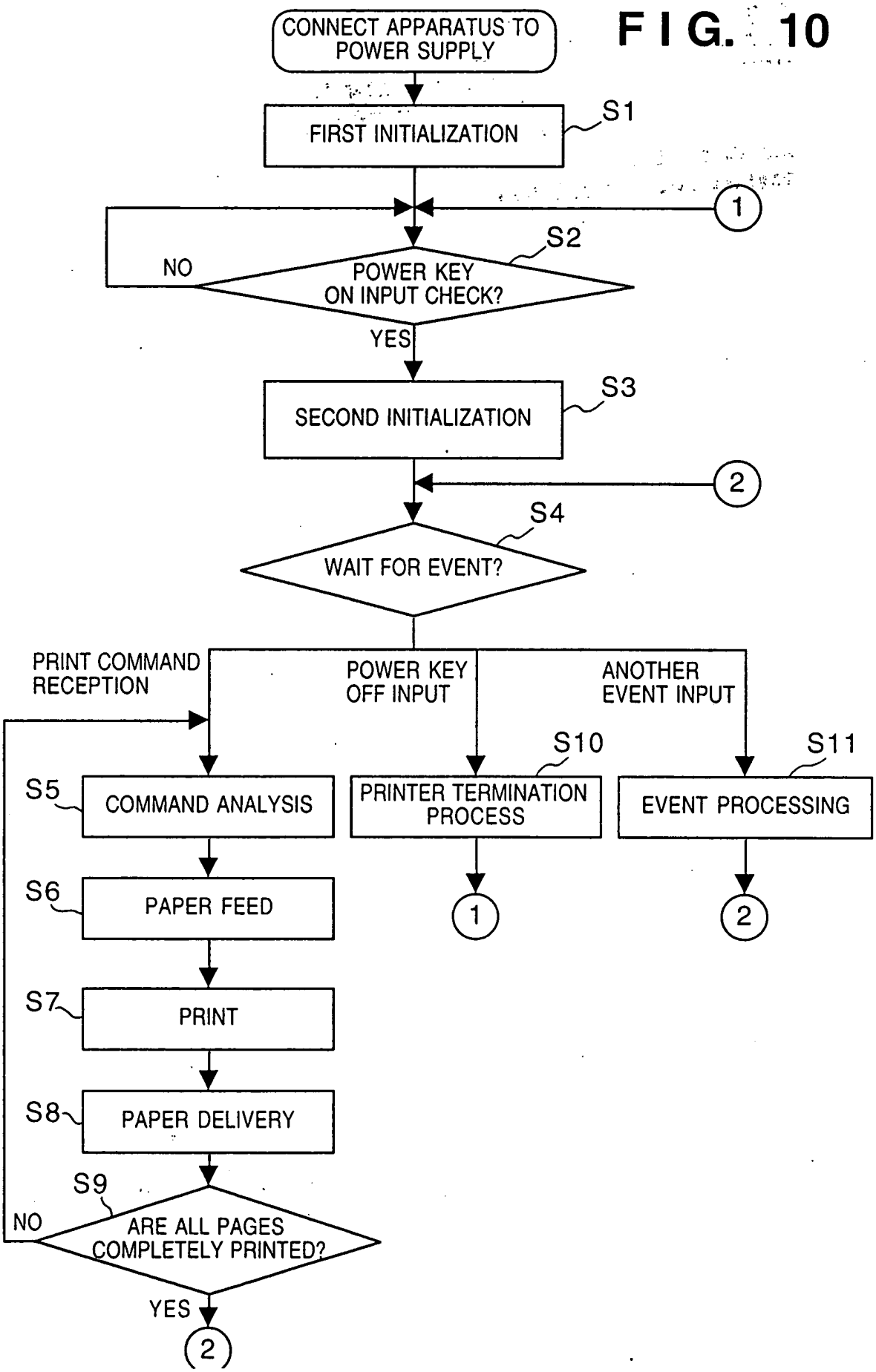


FIG. 11

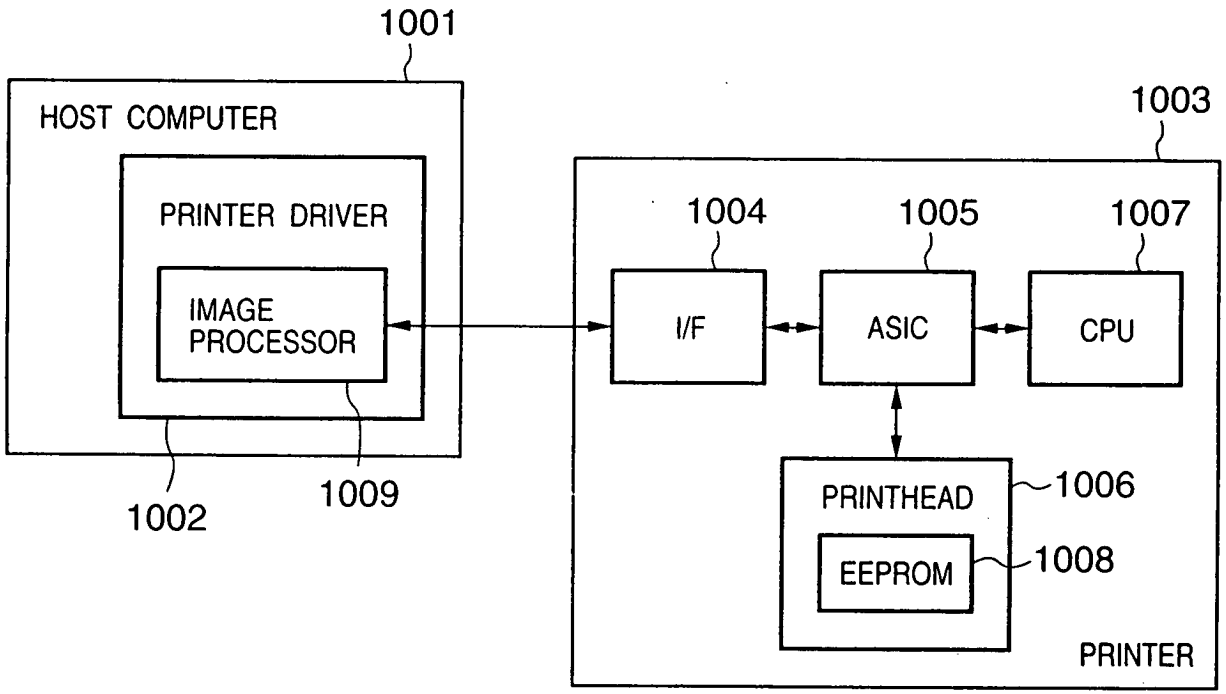


FIG. 12

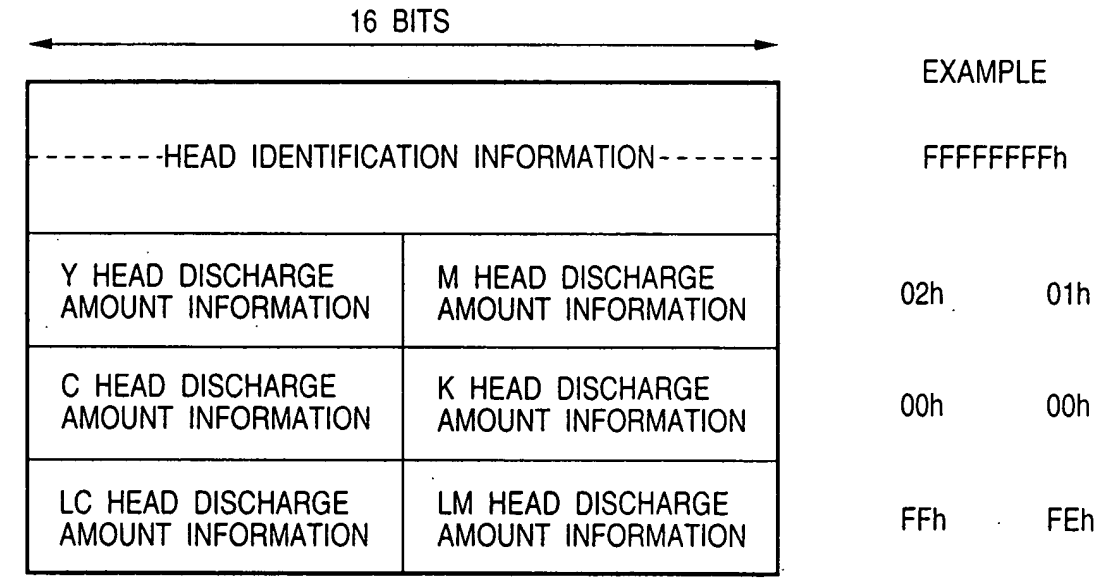

16 BITS
EXAMPLE
HEAD IDENTIFICATION INFORMATION
FFFFFFFFh
Y HEAD DISCHARGE AMOUNT INFORMATION
M HEAD DISCHARGE AMOUNT INFORMATION
02h
01h
C HEAD DISCHARGE AMOUNT INFORMATION
K HEAD DISCHARGE AMOUNT INFORMATION
00h
00h
LC HEAD DISCHARGE AMOUNT INFORMATION
LM HEAD DISCHARGE AMOUNT INFORMATION
FFh
FEh

FIG. 13

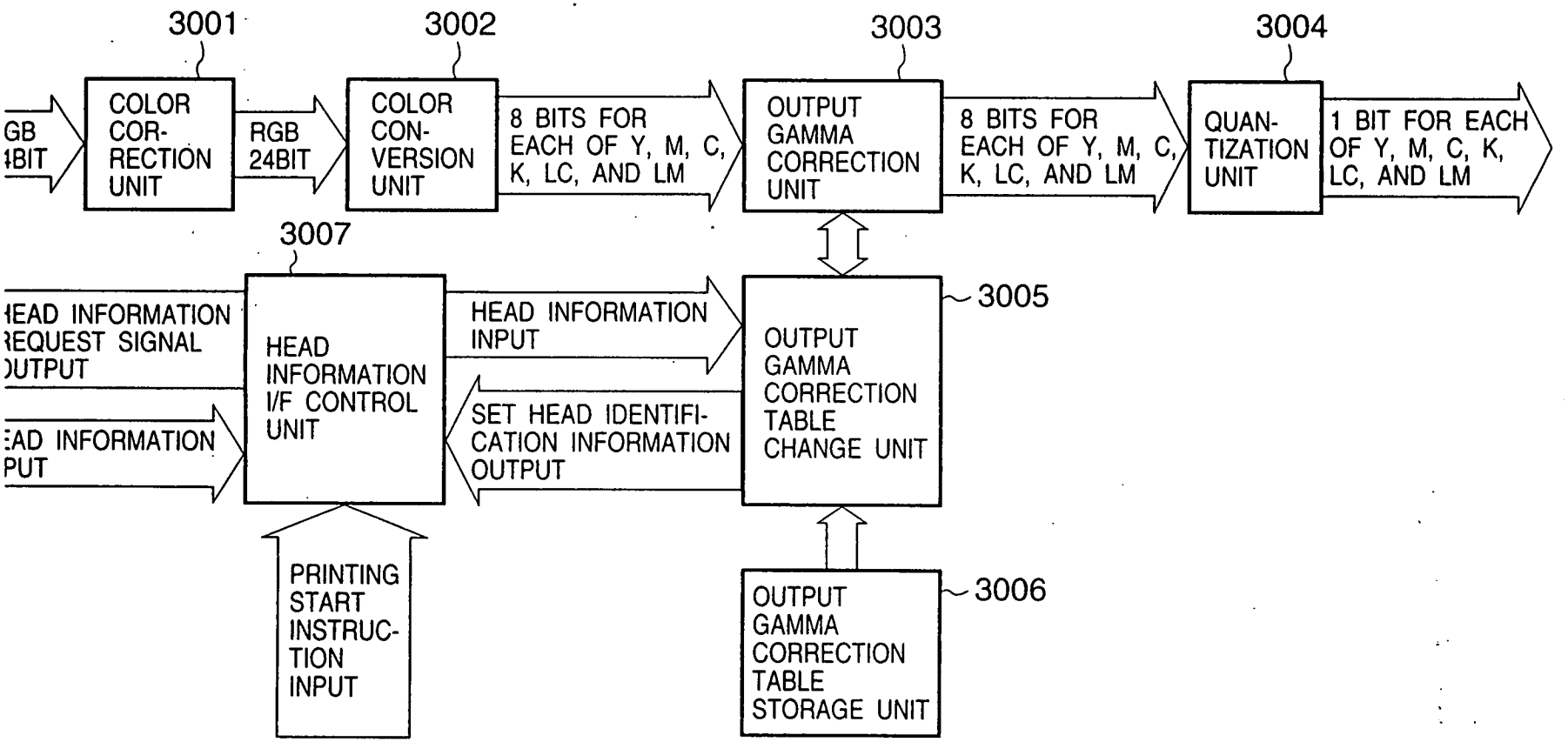


FIG. 14

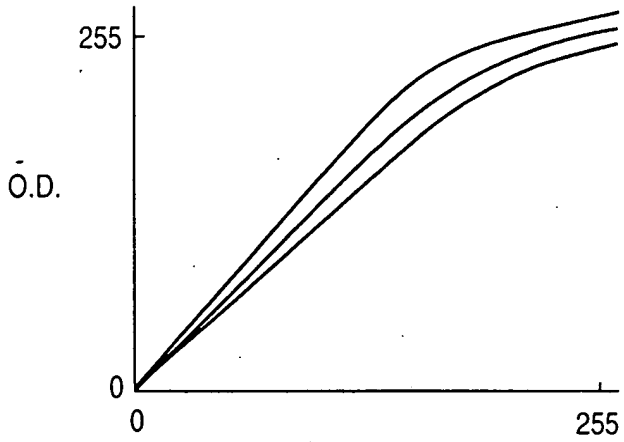


FIG. 15

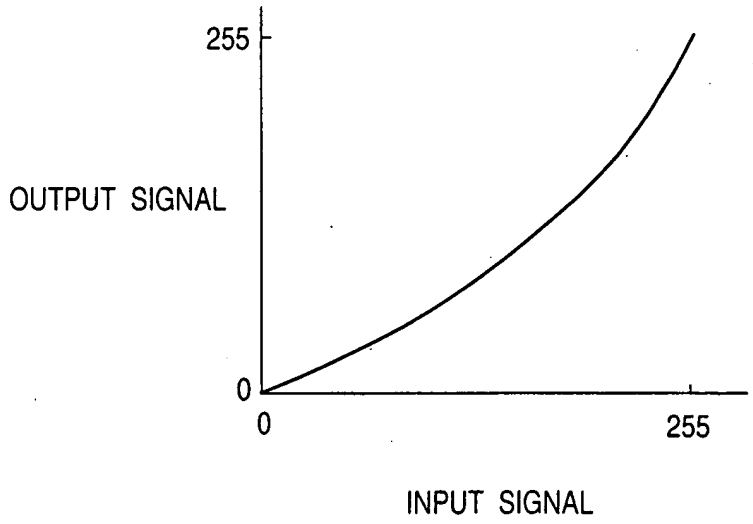


FIG. 16

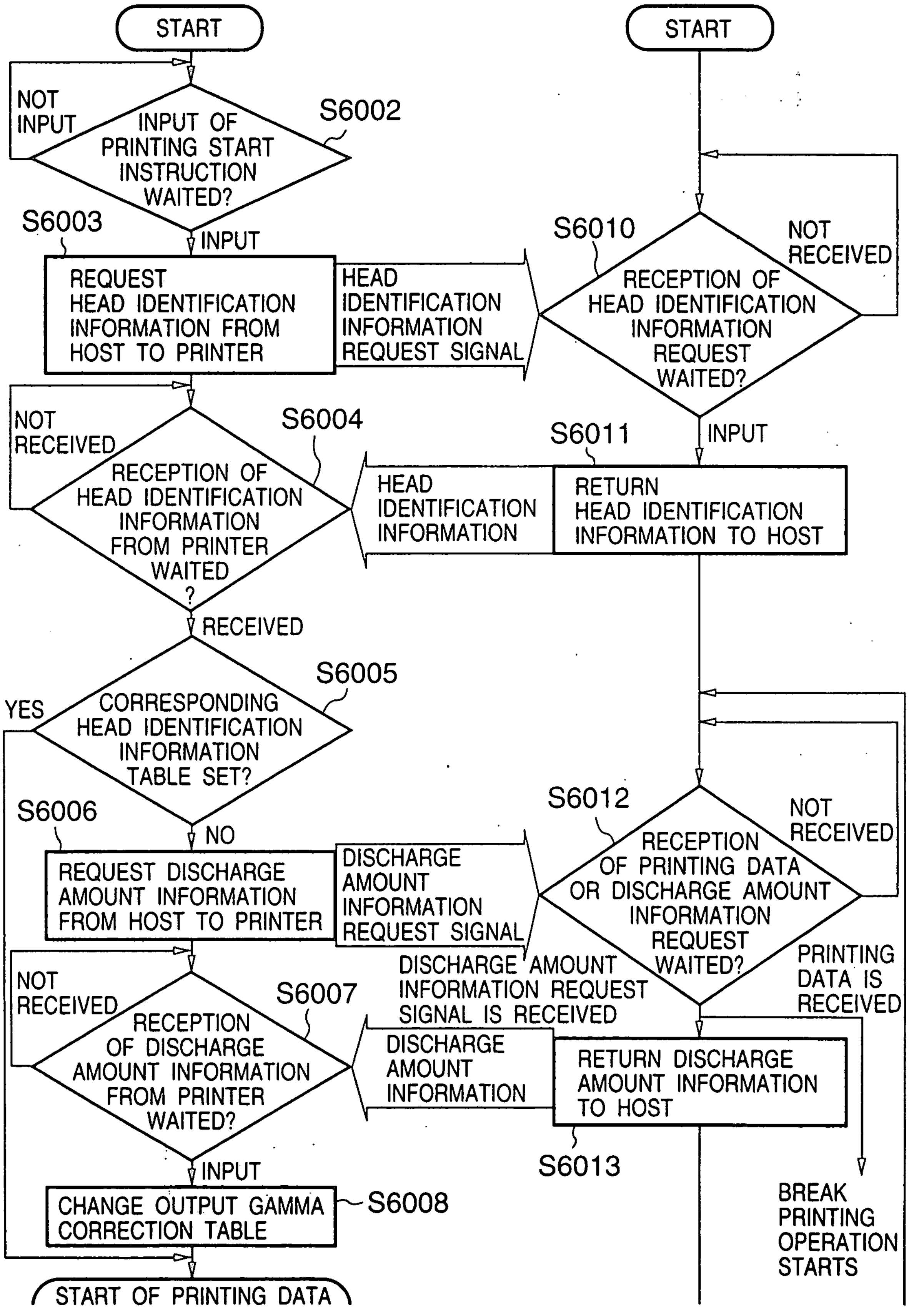


FIG. 17

Y:DISCHARGE AMOUNT-2 LUT	M:DISCHARGE AMOUNT-2 LUT	C:DISCHARGE AMOUNT-2 LUT	K:DISCHARGE AMOUNT-2 LUT	LC:DISCHARGE AMOUNT-2 LUT	LM:DISCHARGE AMOUNT-2 LUT
Y:DISCHARGE AMOUNT-1 LUT	M:DISCHARGE AMOUNT-1 LUT	C:DISCHARGE AMOUNT-1 LUT	K:DISCHARGE AMOUNT-1 LUT	LC:DISCHARGE AMOUNT-1 LUT	LM:DISCHARGE AMOUNT-1 LUT
Y:DISCHARGE AMOUNT 0 LUT	M:DISCHARGE AMOUNT 0 LUT	C:DISCHARGE AMOUNT 0 LUT	K:DISCHARGE AMOUNT 0 LUT	LC:DISCHARGE AMOUNT 0 LUT	LM:DISCHARGE AMOUNT 0 LUT
Y:DISCHARGE AMOUNT+1 LUT	M:DISCHARGE AMOUNT+1 LUT	C:DISCHARGE AMOUNT+1 LUT	K:DISCHARGE AMOUNT+1 LUT	LC:DISCHARGE AMOUNT+1 LUT	LM:DISCHARGE AMOUNT+1 LUT
Y:DISCHARGE AMOUNT+2 LUT	M:DISCHARGE AMOUNT+2 LUT	C:DISCHARGE AMOUNT+2 LUT	K:DISCHARGE AMOUNT+2 LUT	LC:DISCHARGE AMOUNT+2 LUT	LM:DISCHARGE AMOUNT+2 LUT

FIG. 18

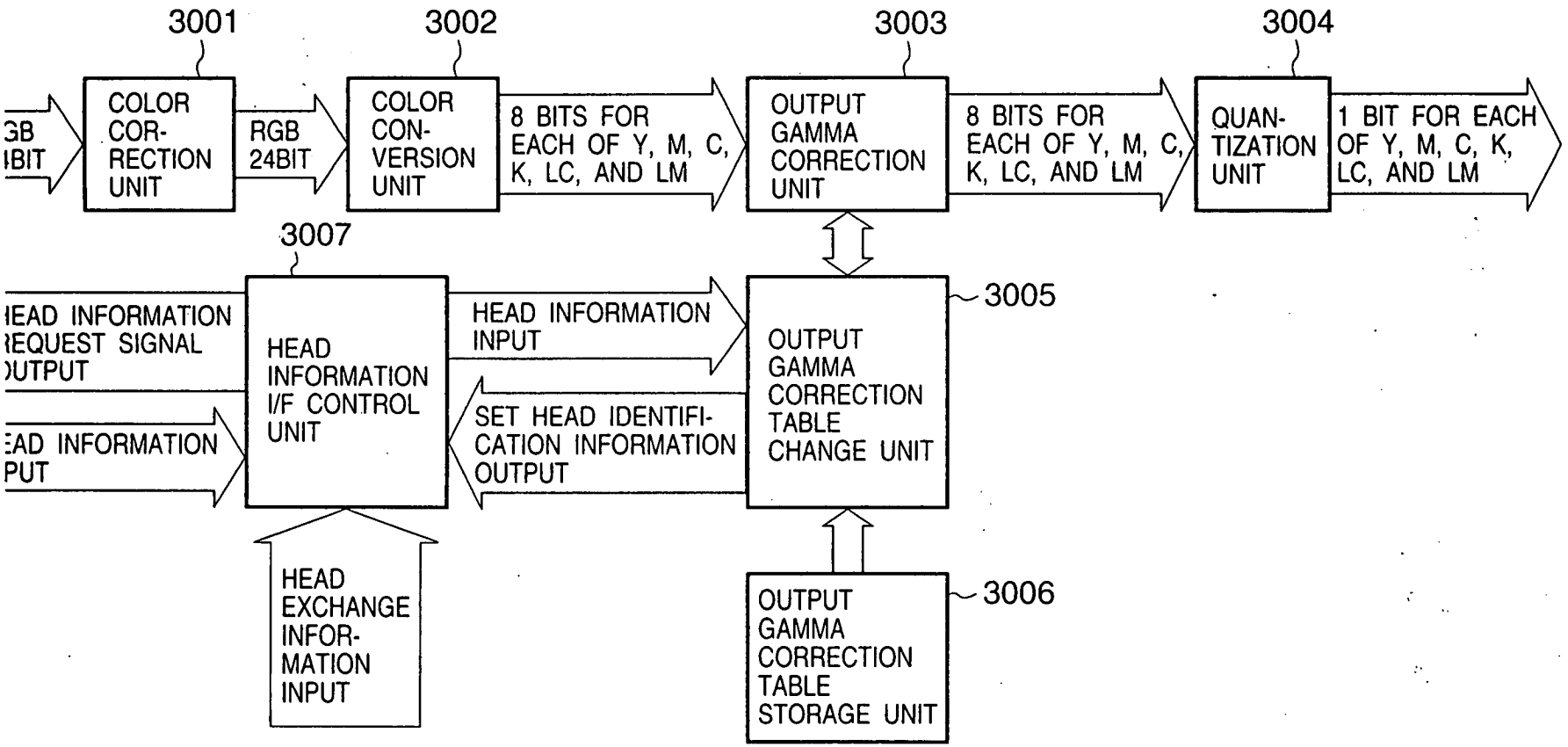

3001
3002
3003
3004
GB
IBIT
COLOR COR-RECTION UNIT
RGB 24BIT
COLOR CON-VERSION UNIT
8 BITS FOR EACH OF Y, M, C, K, LC, AND LM
OUTPUT GAMMA CORRECTION UNIT
8 BITS FOR EACH OF Y, M, C, K, LC, AND LM
QUAN-TIZATION UNIT
1 BIT FOR EACH OF Y, M, C, K, LC, AND LM
3007
3005
EAD INFORMATION
EQUEST SIGNAL
UTPUT
AD INFORMATION
PUT
HEAD INFORMATION I/F CONTROL UNIT
HEAD INFORMATION INPUT
SET HEAD IDENTIFI-CATION INFORMATION OUTPUT
OUTPUT GAMMA CORRECTION TABLE CHANGE UNIT
HEAD EXCHANGE INFOR-MATION INPUT
3006
OUTPUT GAMMA CORRECTION TABLE STORAGE UNIT

FIG. 19

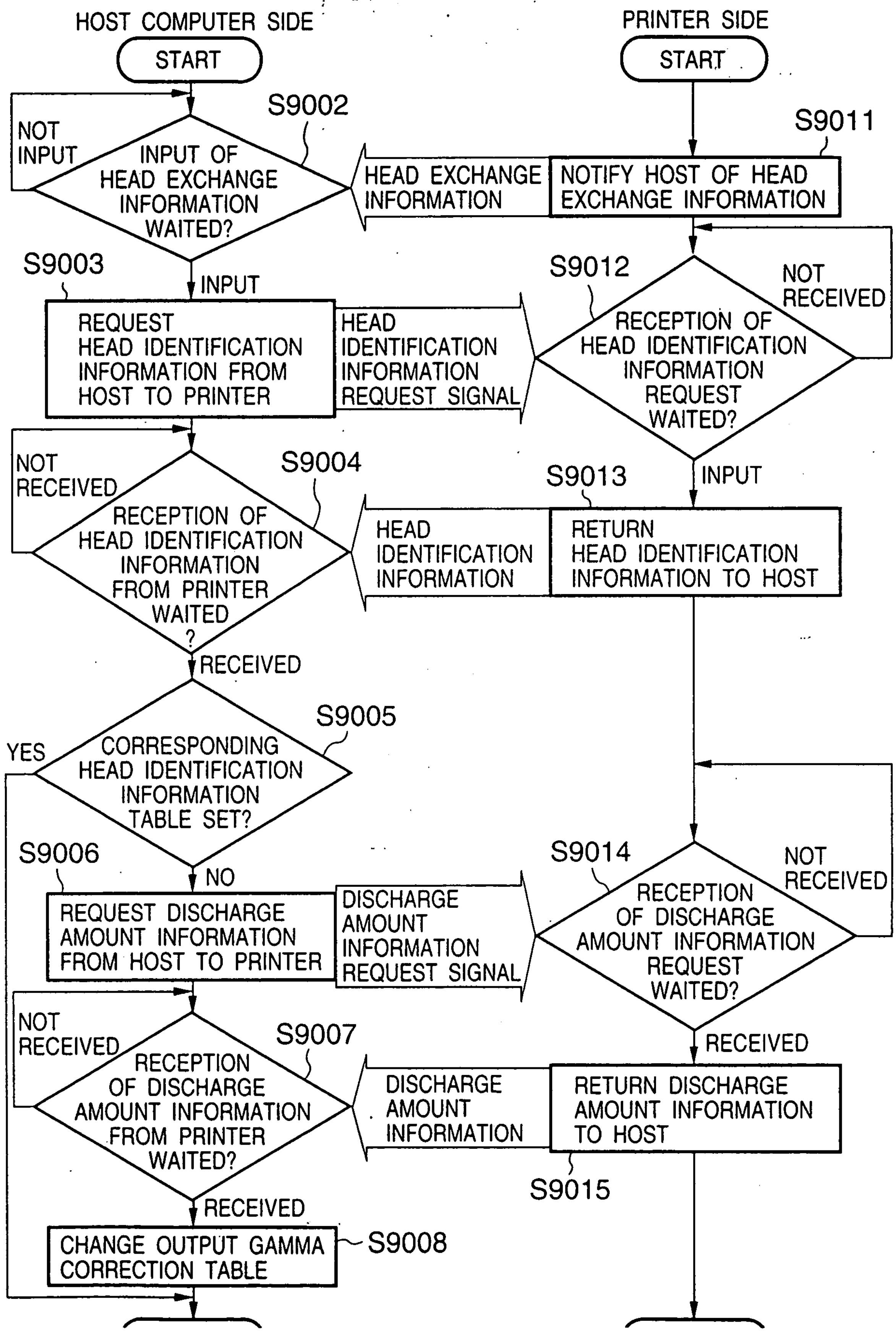
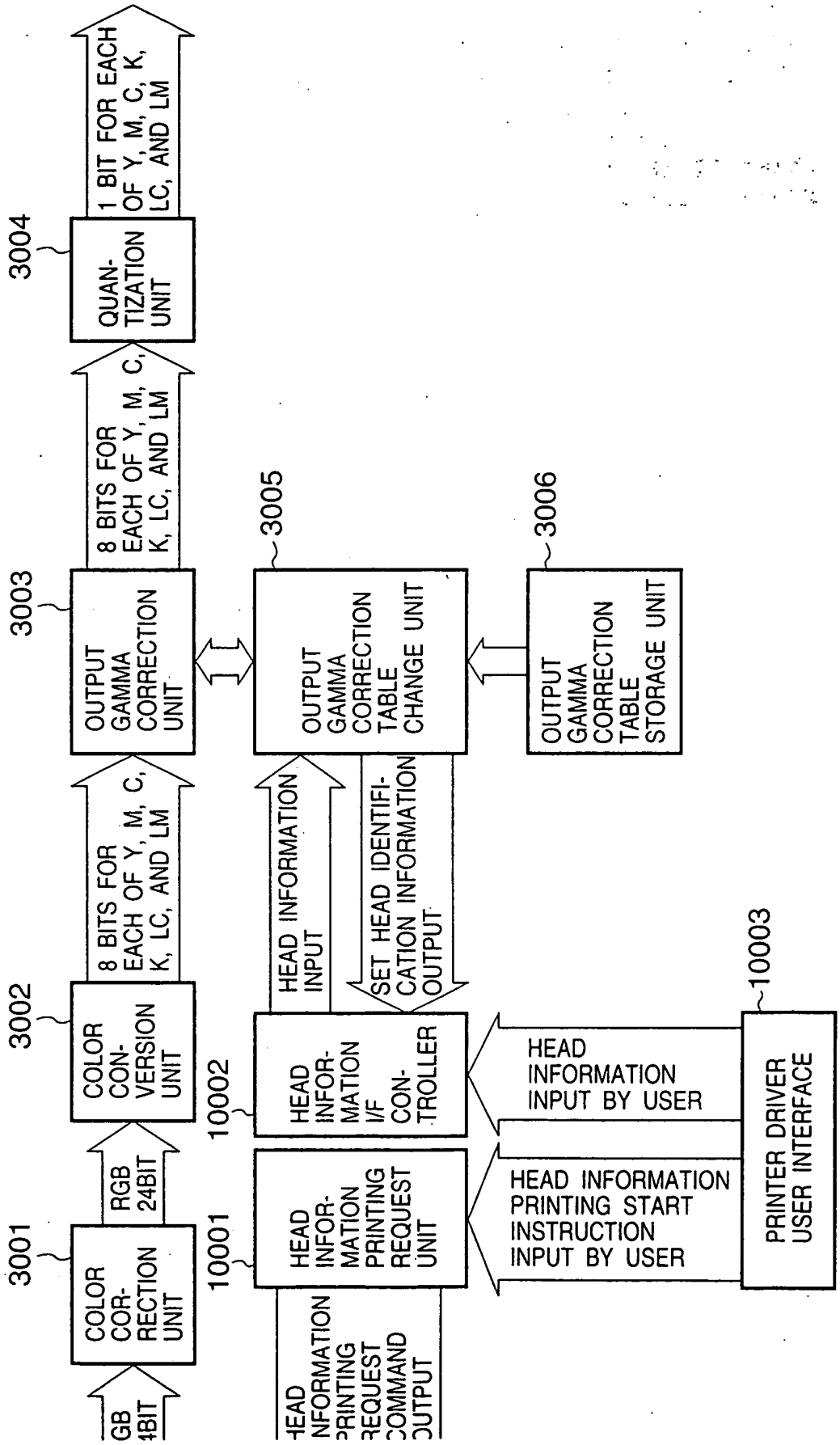


FIG. 20


3001
COLOR COR-RECTION UNIT
RGB 24BIT
3002
COLOR CON-VERSION UNIT
8 BITS FOR EACH OF Y, M, C, K, LC, AND LM
3003
OUTPUT GAMMA CORRECTION UNIT
8 BITS FOR EACH OF Y, M, C, K, LC, AND LM
3004
QUAN-TIZATION UNIT
1 BIT FOR EACH OF Y, M, C, K, LC, AND LM
3005
OUTPUT GAMMA CORRECTION TABLE CHANGE UNIT
3006
OUTPUT GAMMA CORRECTION TABLE STORAGE UNIT
10001
HEAD INFOR-MATION PRINTING REQUEST UNIT
10002
HEAD INFOR-MATION I/F CON-TROLLER
HEAD INFORMATION INPUT
SET HEAD IDENTIFI-CATION INFORMATION OUTPUT
HEAD INFORMATION PRINTING REQUEST COMMAND OUTPUT
HEAD INFORMATION INPUT BY USER
HEAD INFORMATION PRINTING START INSTRUCTION INPUT BY USER
10003
PRINTER DRIVER USER INTERFACE

F.I.G. 21

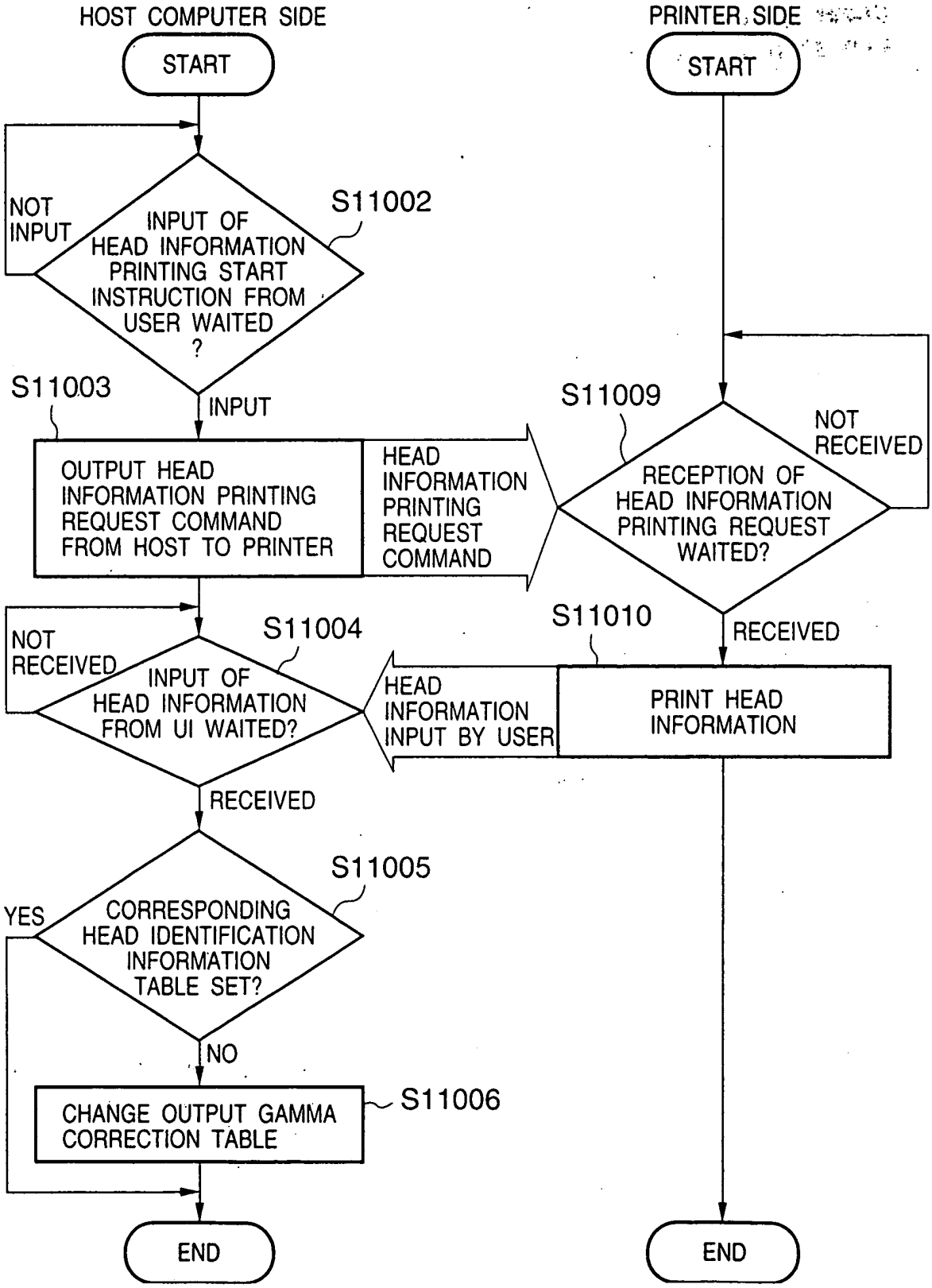

HOST COMPUTER SIDE
START
NOT INPUT
INPUT OF HEAD INFORMATION PRINTING START INSTRUCTION FROM USER WAITED ?
S11002
INPUT
S11003
OUTPUT HEAD INFORMATION PRINTING REQUEST COMMAND FROM HOST TO PRINTER
HEAD INFORMATION PRINTING REQUEST COMMAND
NOT RECEIVED
INPUT OF HEAD INFORMATION FROM UI WAITED?
S11004
RECEIVED
CORRESPONDING HEAD IDENTIFICATION INFORMATION TABLE SET?
S11005
YES
NO
CHANGE OUTPUT GAMMA CORRECTION TABLE
S11006
END
PRINTER SIDE
START
S11009
RECEPTION OF HEAD INFORMATION PRINTING REQUEST WAITED?
NOT RECEIVED
S11010
RECEIVED
HEAD INFORMATION INPUT BY USER
PRINT HEAD INFORMATION
END

FIG. 22

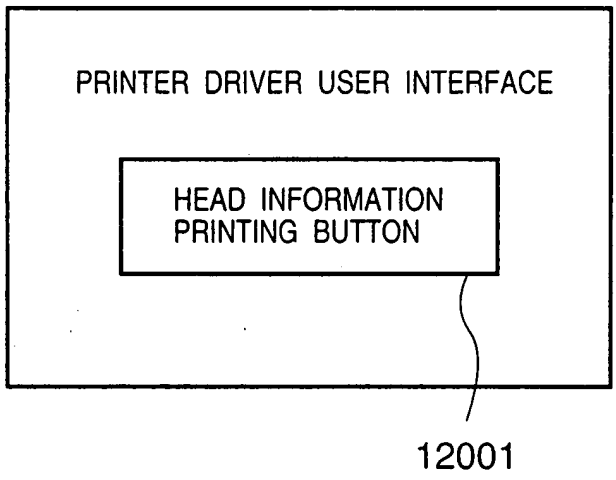


FIG. 23

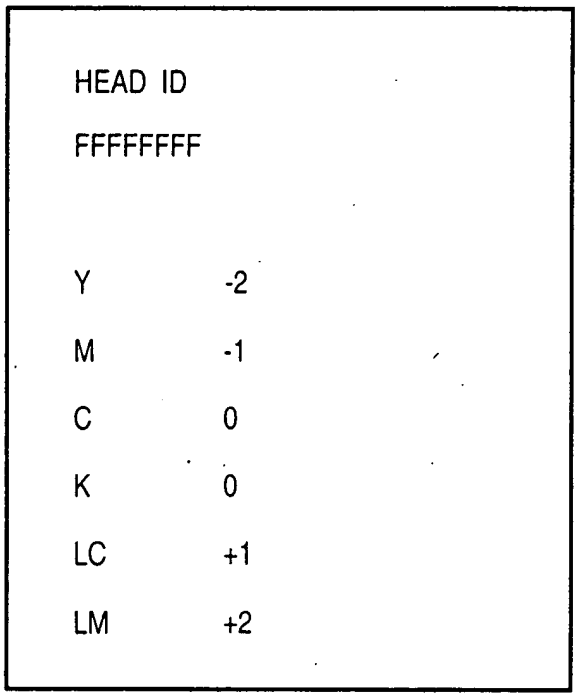


FIG. 24